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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/919,372	07/31/2001	Vadim Gutnik	5347-205	2525	
20792 7	20792 7590 12/12/2006			EXAMINER	
MYERS BIGEL SIBLEY & SAJOVEC			CHUNG, PHUNG M		
PO BOX 37428 RALEIGH, NC 27627			ART UNIT	PAPER NUMBER	
ia ibbidii, iii	2,02.		2138		
·			DATE MAILED: 12/12/2006		

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)		
·	09/919,372	GUTNIK ET AL.		
Office Action Summary	Examiner	Art Unit		
	Phung My Chung	2138		
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address		
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tim vill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).		
Status				
Responsive to communication(s) filed on 11 Sec 2a) This action is FINAL. 2b) This 3) Since this application is in condition for alloware closed in accordance with the practice under E	action is non-final. nce except for formal matters, pro			
Disposition of Claims				
4) Claim(s) 1-26 is/are pending in the application. 4a) Of the above claim(s) is/are withdraw 5) Claim(s) 1-24 is/are allowed. 6) Claim(s) 25-26 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or	vn from consideration.			
Application Papers				
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) access Applicant may not request that any objection to the Replacement drawing sheet(s) including the correction of the oath or declaration is objected to by the Examine 11).	epted or b) objected to by the bedrewing(s) be held in abeyance. See ion is required if the drawing(s) is obj	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).		
Priority under 35 U.S.C. § 119				
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 				
Attachment(s)				
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	ate		

Page 2

Application/Control Number: 09/919,372

Art Unit: 2138

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 25-26 are rejected under 35 U.S.C. 102(b) as being anticipated by Kuddes (5,638,410).

As per claim 26, Kuddes discloses a system for distributing a clock signal over an integrated circuit, comprising:

a means for independently generating a plurality of clock signals (clock signal \$\psi\$1 and clock signal \$\psi2) on the integrated circuit...; and

a means (microcontroller 130) for synchronizing phases of the plurality of clock signals to one another based on error signals that are generated based on relative phase different (phase detector 124) between ones of the plurality of clock signals. (See col. 3, lines 21-23, lines 60-63, col. 4, lines 50-67 to col. 5, lines 1-42).

As per claim 25, this method claim is also rejected under the same rationale as set forth in the system claim 26.

3. Claims 1-24 are allowable.

Application/Control Number: 09/919,372 Page 3

Art Unit: 2138

4. Applicant's arguments with respect to claims 1-26 have been considered but are moot in

view of the new ground(s) of rejection.

5. Any inquiry concerning this communication or earlier communications from the examiner

should be directed to Phung My Chung whose telephone number is 571-272-3818. The examiner

can normally be reached on Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Albert Decady can be reached on 571-272-3819. The fax phone number for the

organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Primary Patent Examiner